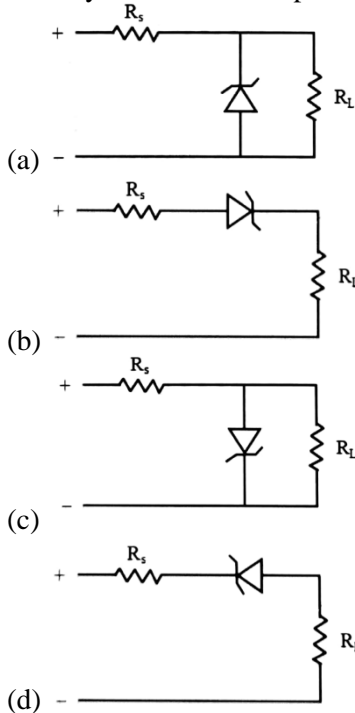


SEMICONDUCTOR, LOGIC GATES & EM WAVE

1. A zener diode is to be used as a voltage regulator. Identify the correct set up -

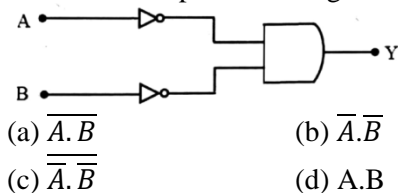


2. **Statement 1** : Conductivity of semiconductors decreases with increase in temperature.

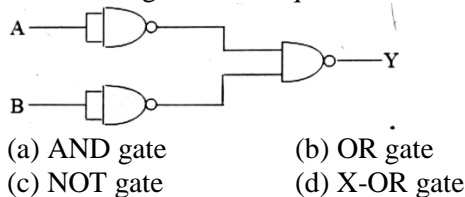
Statement 2 : More electron goes from valance band to conduction band with increase in temperature.

- (a) Both Statement-1 and Statement-2 are true, and Statement-2 is the correct explanation of Statement-1.
 (b) Both Statement-1 and Statement-2 are true but Statement-2 is not the correct explanation of Statement-1.
 (c) Statement-1 is true but Statement-2 is false
 (d) Statement-1 is false but Statement-2 is true.

3. What is out put Y of the gate circuit shown in figure?



4. Following circuit is equivalent to -



5. Depletion layer in the p-n junction consists of -

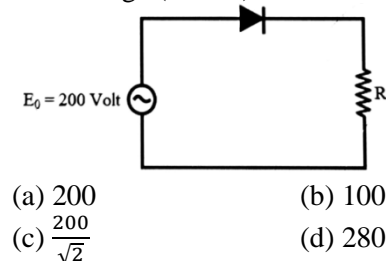
- (a) electrons
 (b) holes

- (c) positive and negative ions fixed in their position
 (d) both electron and holes

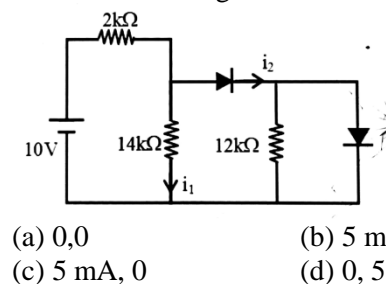
6. The depletion layer in silicon diode is $1\mu\text{m}$ wide and the knee potential is 0.6V , then the electric field in the depletion layer will be -

- (a) Zero (b) 0.6Vm^{-1}
 (c) $6 \times 10^4 \text{ V/m}$ (d) $6 \times 10^5 \text{ V/m}$

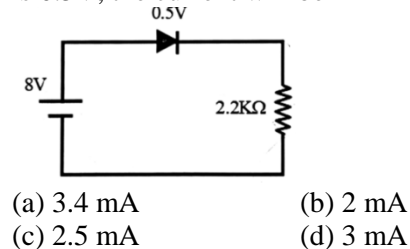
7. A sinusoidal voltage of peak value 200 volt is connected to a diode and resistor R in the circuit shown so that half wave rectification occurs. If the forward resistance of the diode is negligible compared to R the rms voltage (in volt) across R is approximately -



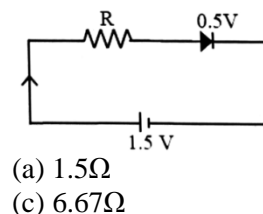
8. In the following circuit find i_1 and i_2 -



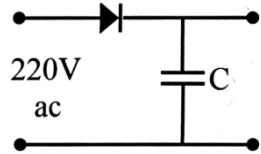
9. In the circuit, if the forward voltage drop for the diode is 0.5V , the current will be-



10. The diode used in the circuit shown in the figure has a constant voltage drop of 0.5V at all currents and a maximum power rating of 100 milliwatts. What should be the value of the resistor R, connected in series with the diode for obtaining maximum current -



11. A diode is connected to 220 V (rms) ac in series with a capacitor as shown in figure. The rms voltage across the capacitor is -



- (a) 220 V
(b) 110 V
(c) 311.1 V
(d) $\frac{220}{\sqrt{2}}$ V

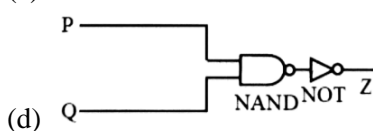
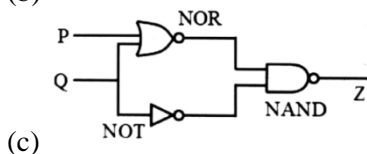
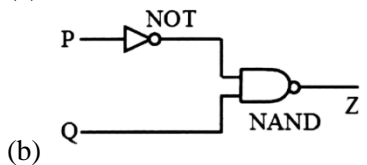
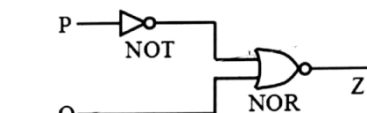
12. The following configuration of gate is equivalent to-

- (a) NAND
(b) XOR
(c) OR
(d) None of these

13. A combination of logic gates has the truth table below.

P	Q	Z
0	0	0
0	1	1
1	0	1
1	1	1

Which of the following combinations has this truth table?



14. In a p-n junction diode the direction of diffusion current is from -

- (a) p-region to n-region
(b) n-region to p-region
(c) n-region to p-region when forward biased and vice-versa when reverse biased
(d) p-region to n-region when forward biased and vice-versa when reverse biased

15. Choose only false statement from the following -

- (a) In conductors the valence and conduction band overlap
(b) Substance with energy gap of the order of 10 eV are insulators

(c) The resistivity of a semi conductor increase with increase in temperature

(d) The conductivity of semiconductor increase with increase in temperature

16. Match column 1 with column 2

Column I	Column II
(A)	(P) $Z = P + \bar{Q}$
(B)	(Q) $Z = P + Q$
(C)	(R) $Z = \bar{P} \cdot \bar{Q}$
(D)	(S) $Z = (P \cdot \bar{Q})$

- (a) $A \rightarrow R; B \rightarrow P; C \rightarrow Q; D \rightarrow S$
(b) $A \rightarrow S; B \rightarrow P; C \rightarrow Q; D \rightarrow R$
(c) $A \rightarrow S; B \rightarrow P; C \rightarrow R; D \rightarrow Q$
(d) $A \rightarrow P; B \rightarrow S; C \rightarrow Q; D \rightarrow R$

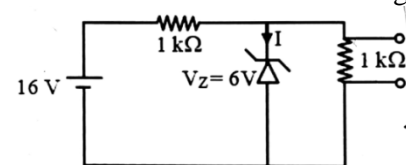
17. How many minimum "NOR" gates are required to make one "NAND" gate-

- (a) 1
(b) 2
(c) 3
(d) 4

18. Which statement is correct for p-type semiconductor-
(a) the number of electrons in conduction band is more than the number of holes in valence band at room temperature

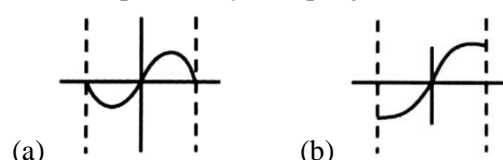
- (b) the number of holes in valence band is more than the number of electrons in conduction band at room temperature
(c) there are no holes and electrons at room temperature
(d) number of holes and electrons is equal in valence and conduction band

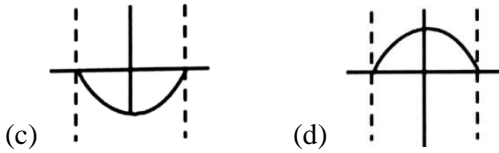
19. What is value of current I in given circuit -



- (a) 6 mA
(b) 4mA
(c) 10 mA
(d) zero

20. Which graph shows correct variation of electric field across depletion layer of p-n junction-





21. The maxwell's equation:

$\oint \vec{B} \cdot d\vec{l} = \mu_0 \left(i + \epsilon_0 \cdot \frac{d\phi_E}{dt} \right)$ is a statement of-

- (a) Faraday's law of induction
- (b) Modified Ampere's law
- (c) Gauss's law of electricity
- (d) Gauss's law of magnetism

22. The relation between electric field E and magnetic field H in an electromagnetic wave is-

- (a) $E = H$
- (b) $E = \frac{\mu_0}{\epsilon_0} H$
- (c) $E = \sqrt{\frac{\mu_0}{\epsilon_0}} H$
- (d) $E = \sqrt{\frac{\epsilon_0}{\mu_0}} H$

23. The relation between electric field E and magnetic field induction B in an electromagnetic waves-

- (a) $E = \sqrt{\frac{\mu_0}{\epsilon_0}} B$
- (b) $E = cB$
- (c) $E = \frac{B}{c}$
- (d) $E = \frac{B}{c^2}$

24. An electromagnetic wave going through vacuum is described by-

$$E = E_0 \sin(kx - \omega t)$$

$$B = B_0 \sin(kx - \omega t)$$

- (a) $E_0 B_0 = \omega k$
- (b) $E_0 \omega = B_0 k$
- (c) $E_0 k = B_0 \omega$
- (d) none of these

25. A plane E M wave of frequency 25 MHz travels in free space in x direction. At a particular point in space and time $E = 6.3 \hat{j}$ v/m then B at that point is -

- (a) $2.1 \times 10^{-8} \hat{j}$
- (b) $2.1 \times 10^{-8} \hat{k}$
- (c) $2.1 \hat{k}$
- (d) $2.1 \times 10^{-8} \hat{i}$

26. The average value of electric energy density in an electromagnetic wave is (E_0 is peak value)-

- (a) $\frac{1}{2} \epsilon_0 E_0^2$
- (b) $\frac{E_0^2}{2\epsilon_0}$
- (c) $\epsilon_0 E_0^2$
- (d) $\frac{1}{4} \epsilon_0 E_0$

27. A lamp radiates power P_0 uniformly in all directions, the amplitude of electric field strength E_0 at a distance r from it is-

- (a) $E_0 = \frac{P_0}{2\pi\epsilon_0 cr^2}$
- (b) $E_0 = \sqrt{\left\{ \frac{P_0}{2\pi\epsilon_0 cr^2} \right\}}$
- (c) $E_0 = \sqrt{\left\{ \frac{P_0}{4\pi\epsilon_0 cr^2} \right\}}$
- (d) $E_0 = \sqrt{\left\{ \frac{P_0}{8\pi\epsilon_0 cr^2} \right\}}$

28. A parallel plate capacitor consists of two circular plates each of radius 2 cm, separated by a distance of 0.1

mm. If voltage across the plates is varying at the rate of 5×10^{13} V/s, then the value of displacement current is-*

- (a) 5.50A
- (b) 5.56×10^2 A
- (c) 5.56×10^3 A
- (d) 2.28×10^4 A

29. In an electromagnetic wave-

- (a) Power is transmitted along the magnetic field
- (b) power is transmitted along the electric field
- (c) power is equally transferred along the electric and magnetic fields
- (d) power is transmitted in a direction perpendicular to both the fields

30. For any E.M. wave if $E = 100$ V/m and $B = 3.33 \times 10^{-7}$ T. Then the rate of energy flow per unit area is-

- (a) 3.33×10^{-5} J/m²
- (b) 26.5 VA/m²
- (c) 3×10^8 J/m²
- (d) None of these

31. In an electromagnetic wave, the amplitude of electric field is 10 V/m. The frequency of wave is 5×10^{14} Hz, the wave is propagating along z-axis, then total average energy density of E.M. wave is -

- (a) 2.21×10^{-10} J/m³
- (b) 4.42×10^{-10} J/m³
- (c) 1.11×10^{-10} J/m³
- (d) None

32. The resonance frequency of the tank circuit of an oscillator when

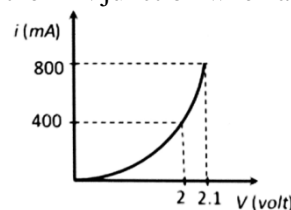
$L = \frac{10}{\pi^2}$ mH and $C = 0.04$ μ F are connected in parallel is

- (a) 250 kHz
- (b) 25 kHz
- (c) 2.5 kHz
- (d) 25 MHz

33. Semiconductor is damaged by the strong current due to

- (a) Lack of free electron
- (b) Excess of electrons
- (c) Excess of proton
- (d) None of these

34. The i-V characteristic of a P-N junction diode is shown below. The approximate dynamic resistance of the P-N junction when a forward bias of 2 volt applied

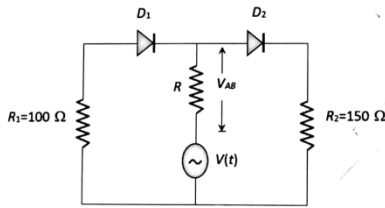


- (a) 1 Ω
- (b) 0.25 Ω
- (c) 0.5 Ω
- (d) 5 Ω

35. Zener breakdown in a semi-conductor diode occurs when

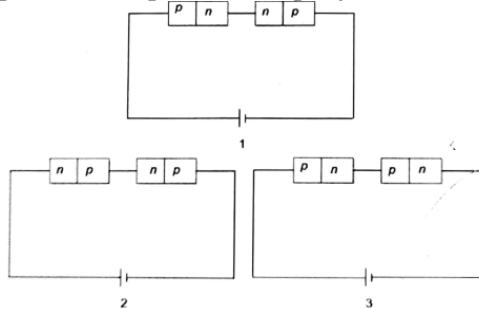
- (a) Forward current exceeds certain value
- (b) Reverse bias exceeds certain value
- (c) Forward bias exceeds certain value
- (d) Potential barrier is reduced to zero

36. In the circuit given below, $V(t)$ is the sinusoidal voltage source, voltage drop $V_{AB}(t)$ across the resistance R is



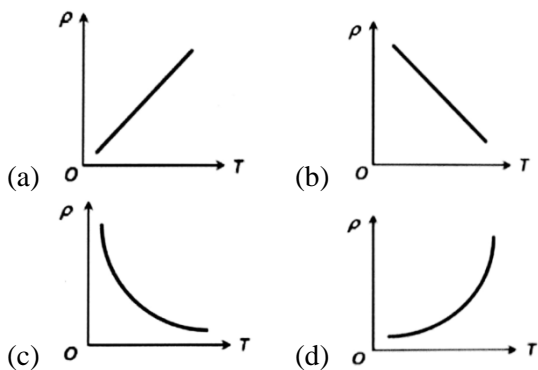
- (a) Is half wave rectified
- (b) Is full wave rectified
- (c) Has the same peak value in the positive and negative half cycles
- (d) Has different peak values during positive and negative half cycle

37. Two identical p-n junctions are connected in series in three different ways as shown below to a battery, the potential drop across the p-n junctions are equal in



- (a) Circuits 2 and 3
- (b) Circuits 1 and 2
- (c) Circuits 1 and 3
- (d) None of the circuit

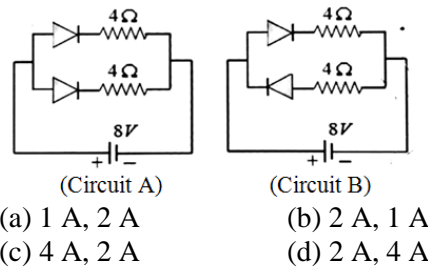
38. The temperature (T) dependence on resistivity (ρ) of a semiconductor is represented by



39. For a common emitter amplifier, the audio signal voltage across the collector resistance $2k\Omega$ is $2V$. If the current amplification factor of the transistor is 220 , and the base resistance is $1.5k\Omega$, the input signal voltage and base current are

- (a) $0.1V$ and $1\mu A$
- (b) $0.15V$ and $10\mu A$
- (c) $1.015V$ and $1\mu A$
- (d) $0.0075V$ and $5\mu A$

40. Currents flowing in each of the circuits A and B respectively are

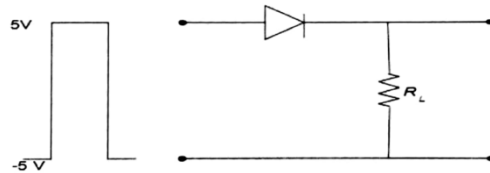


- (a) $1A, 2A$
- (b) $2A, 1A$
- (c) $4A, 2A$
- (d) $2A, 4A$

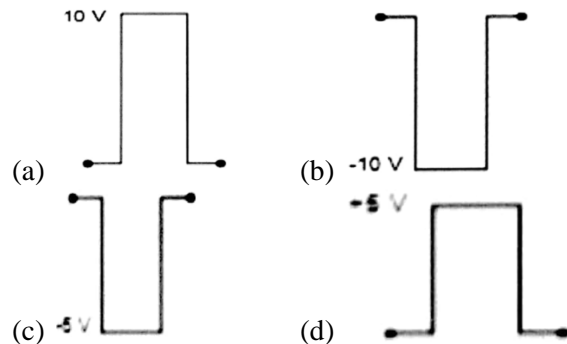
41. For a junction diode the ratio of forward current and reverse current (I_F) is [I_e = electronic charge, V = voltage applied across junction, k = Boltzmann constant, T = temperature in kelvin]

- (a) $e^{-V/kT}$
- (b) $e^{V/kT}$
- (c) $(e^{-V/kT} + 1)$
- (d) $(e^{V/kT} - 1)$

42. If in a p-n junction diode, a square input signal of $10V$ is applied as shown



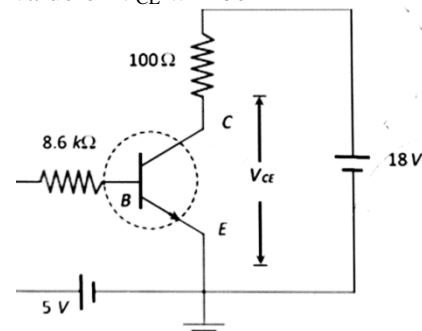
Then the output signal across R_L will be



43. In PN-junction diode the reverse saturation current is 10^{-5} amp at $27^\circ C$. The forward current for a voltage of 0.2 volt is [$\exp(7.62) = 2038.6$, $k = 1.4 \times 10^{-23} \text{ J/K}$]

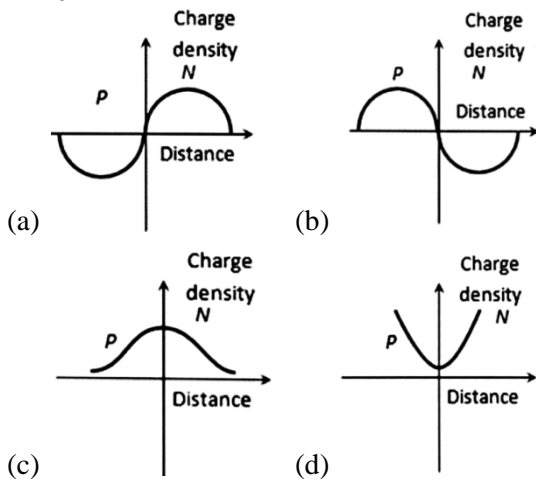
- (a) $2037.6 \times 10^{-3} \text{ amp}$
- (b) $203.76 \times 10^{-3} \text{ amp}$
- (c) $20.376 \times 10^{-3} \text{ amp}$
- (d) $2.0376 \times 10^3 \text{ amp}$

44. For the transistor circuit shown below, if $\beta = 100$, voltage drop between emitter and base is $0.7V$ then value of V_{CE} will be



- (a) $10V$
- (b) $5V$
- (c) $13V$
- (d) $0V$

45. The curve between charge density and distance near P-N junction will be

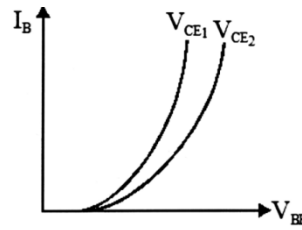


46. **Statement 1** : Doping concentration is maximum in emitter in transistor.

Statement 2 : Maximum number of electrons flows from emitter to base in n-p-n transistor.

- (a) Both Statement-1 and Statement-2 are true, and Statement-2 is the correct explanation of Statement-1.
 (b) Both Statement-1 and Statement-2 are true but Statement-2 is not the correct explanation of Statement-1.
 (c) Statement-1 is true but Statement-2 is false.
 (d) Statement-1 is false but Statement-2 is true.

47. Input characteristics are shown for CE configuration of n-p-n transistor for different output voltages. Here -

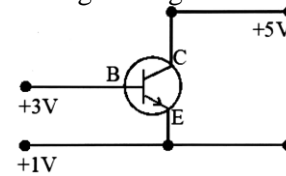


- (a) $V_{CE1} > V_{CE2}$ (b) $V_{CE1} = V_{CE2}$
 (c) $V_{CE1} < V_{CE2}$ (d) None of these

48. Zener diode has both p and fronds heavily doped so that -

- (a) it has small thickness of depletion region
 (b) it has large thickness of depletion region due to large recombination
 (c) it has large reverse bias voltage
 (d) it has weak reverse current when reverse biased

49. In given figure -



- (a) emitter is forward biased
 (b) collector is forward biased
 (c) emitter is reverse biased
 (d) emitter and collector both are reverse biased

50. A condenser is charged using a constant current. The ratio of the magnetic fields at a distance of $R/2$ and R from the axis is (R is the radius of plate)

- (a) 1:1 (b) 2:1
 (c) 1:2 (d) 1:4